



Global Communication Semiconductors, LLC

Corporate & Foundry
23155 Kashiwa Court, Torrance, CA 90505
Telephone: 310-530-7274 Fax: 310-517-8200
Website: www.gcsincorp.com

Sr. Device / Integration Engineer

Job Req. #0712
Department: RF and Power Technology
Status: Full-Time / Salaried
Shift: Day

Job Description

- Lead GaN HEMT device/process development and yield improvement efforts, including 0.5um, 0.25um, and 0.15um GaN HEMT processes.
- Responsible for device fabrication, characterization, and qualification.
- Support trouble-shooting, failure analysis, and yield improvement in related production processes.
- Interact with customers and manufacturing staff.
- Manage R&D projects.

Job Requirements

- Master's degree in Electrical Engineering. Physics or Materials Science
- 10+ years of industry experience in semiconductor wafer fab environment
- 4-6 years of direct hands-on experience in GaN HEMT technology